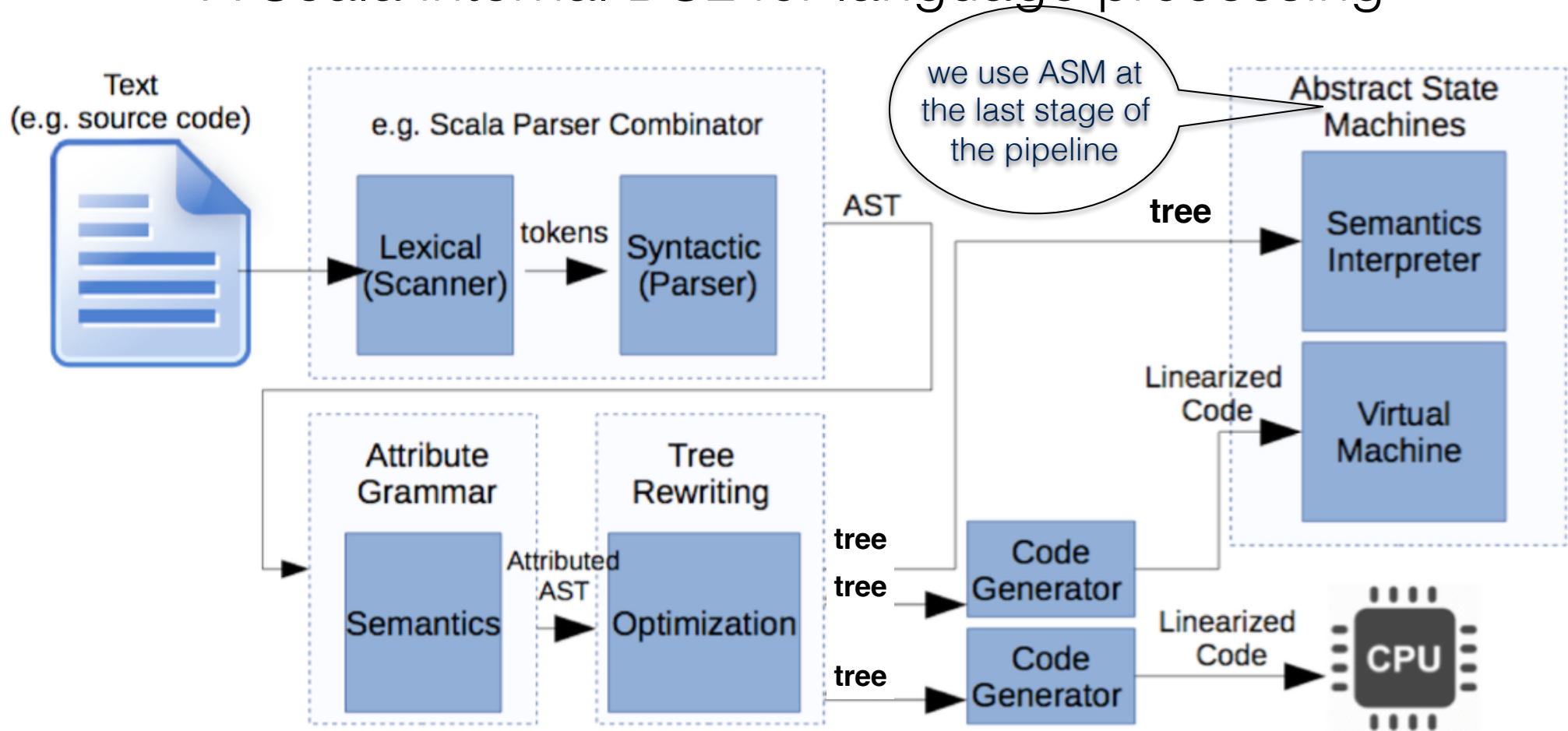


Evaluating Kiama Abstract State Machines for a Java Implementation

Pongsak Suvanpong and Anthony Sloane
Department of Computing, Macquarie University

Kiama

A Scala internal DSL for language processing



An example of a language processing pipeline in
Kiama

Objectives

- We are interested in using Abstract State Machines (ASM) to execute programming languages.
- We want to develop techniques in Scala, so that we can take ASM definitions and quickly code them in Scala.
- Our aim is to be able to closely replicate the ASM definition written in the JBOOK.

Abstract State Machines(ASM)

- “ASM captures in mathematically rigorous yet transparent form some fundamental operational intuitions of computing, and the **notation is familiar from programming practice and mathematical standards.**”[JBOOK]
- Pseudocode (notation) over abstract data (abstract state).
- Discrete time-step execution model.

ASM

State and Rule

- A state in ASM is an n-arity function $f(a_1, a_2, \dots, a_n)$ where a_1, a_2, \dots, a_n are called *locations* and f is the state name
- A state can be thought as a memory unit of ASM which allows the read/write operations. The location abstracts away the memory addressing.
- In each time-step, all rules are executed which may update states.
- An update to a state is not visible until the next time-step.
- Rules are the control logic of ASM.

ASM execution model

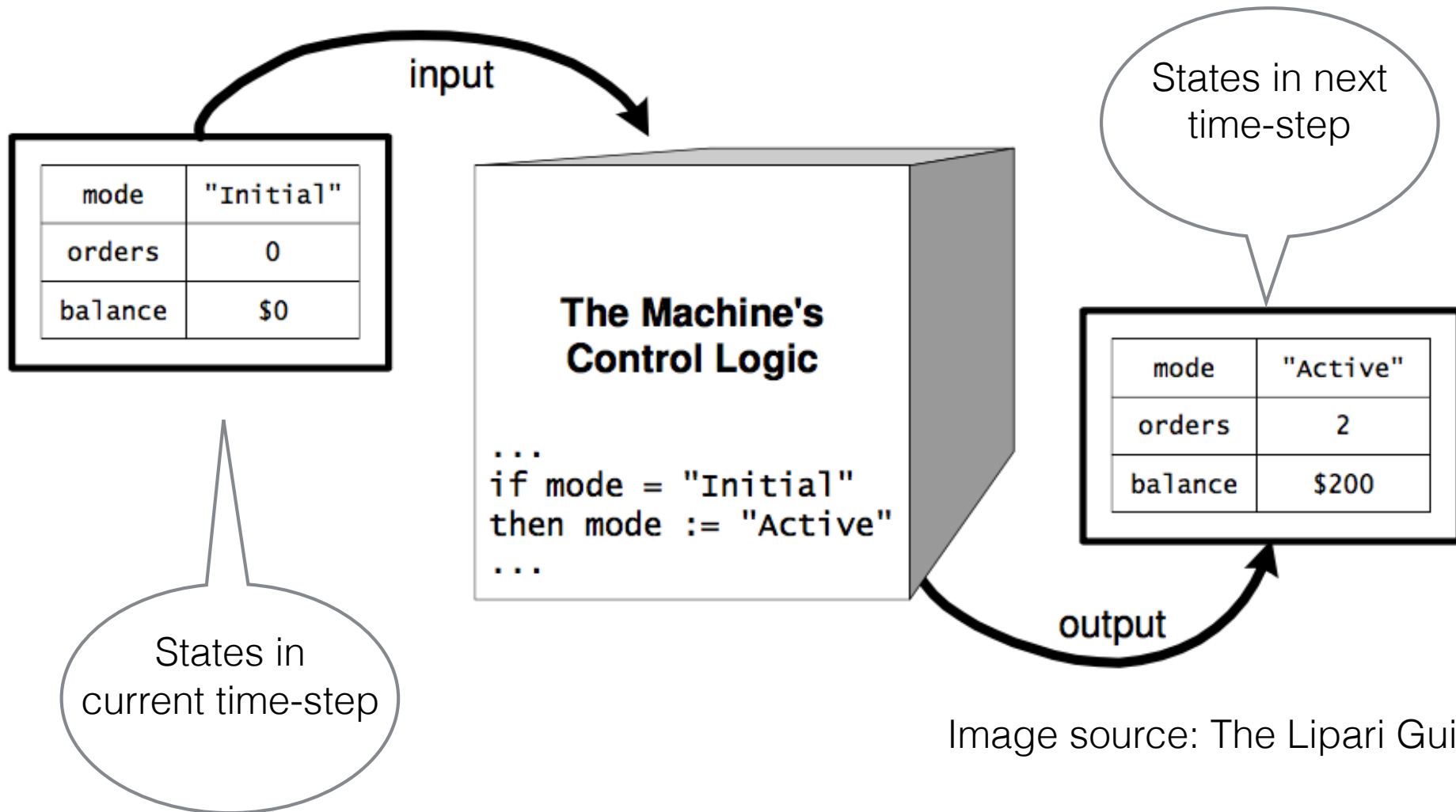


Image source: The Lipari Guide



nullary state

ASM

example: estimate $\log_2(9)$

Function N: \Rightarrow **Number**

InitRule =

N := 9

MainRule =

if N > 1 **then** N := N / 2

else skip

Step	Value of State N
0 (Init)	9
1	4
2	2
3	1

The number of steps is the result.



Function N: \Rightarrow Number

InitRule =

N := 9

MainRule =

if N > 1 then N := N / 2

else skip

Log2 machine in
standard ASM
notation

Log2 machine in
Kiama

```
class Log2ASM(n:Int) extends Machine("Log2ASM")  
{  
  val N = new State[Int]("N")  
  
  override def init(): Unit = N := n  
  override def main(): Unit =  
  {  
    if(N >= 1)  
    | N := N / 2  
  }  
}
```




Java and The Java Virtual Machine (JBOOK).

- It defines ASM definitions of the semantics of the Java language, the compiler and the JVM.
- It mathematically proves that the execution of the semantics ASM and the JVM ASM is equivalent.

$execJavaExp_I = \mathbf{case\ context(pos)\ of}$
 $lit \rightarrow yield(JLS(lit))$

$loc \rightarrow yield(locals(loc))$

$uop^\alpha exp \rightarrow pos := \alpha$

$uop \blacktriangleright val \rightarrow yieldUp(JLS(uop, val))$

$^\alpha exp_1 bop^\beta exp_2 \rightarrow pos := \alpha$

$\blacktriangleright val bop^\beta exp \rightarrow pos := \beta$

$^\alpha val_1 bop \blacktriangleright val_2 \rightarrow \mathbf{if\ } \neg(bop \in divMod \wedge isZero(val_2)) \mathbf{\ then}$
 $yieldUp(JLS(bop, val_1, val_2))$

$loc = ^\alpha exp \rightarrow pos := \alpha$

$loc = \blacktriangleright val \rightarrow locals := locals \oplus \{(loc, val)\}$
 $yieldUp(val)$

$^\alpha exp_0 ? ^\beta exp_1 : ^\gamma exp_2 \rightarrow pos := \alpha$

$\blacktriangleright val ? ^\beta exp_1 : ^\gamma exp_2 \rightarrow \mathbf{if\ } val \mathbf{\ then\ } pos := \beta \mathbf{\ else\ } pos := \gamma$

$^\alpha True ? \blacktriangleright val : ^\gamma exp \rightarrow yieldUp(val)$

$^\alpha False ? ^\beta exp : \blacktriangleright val \rightarrow yieldUp(val)$

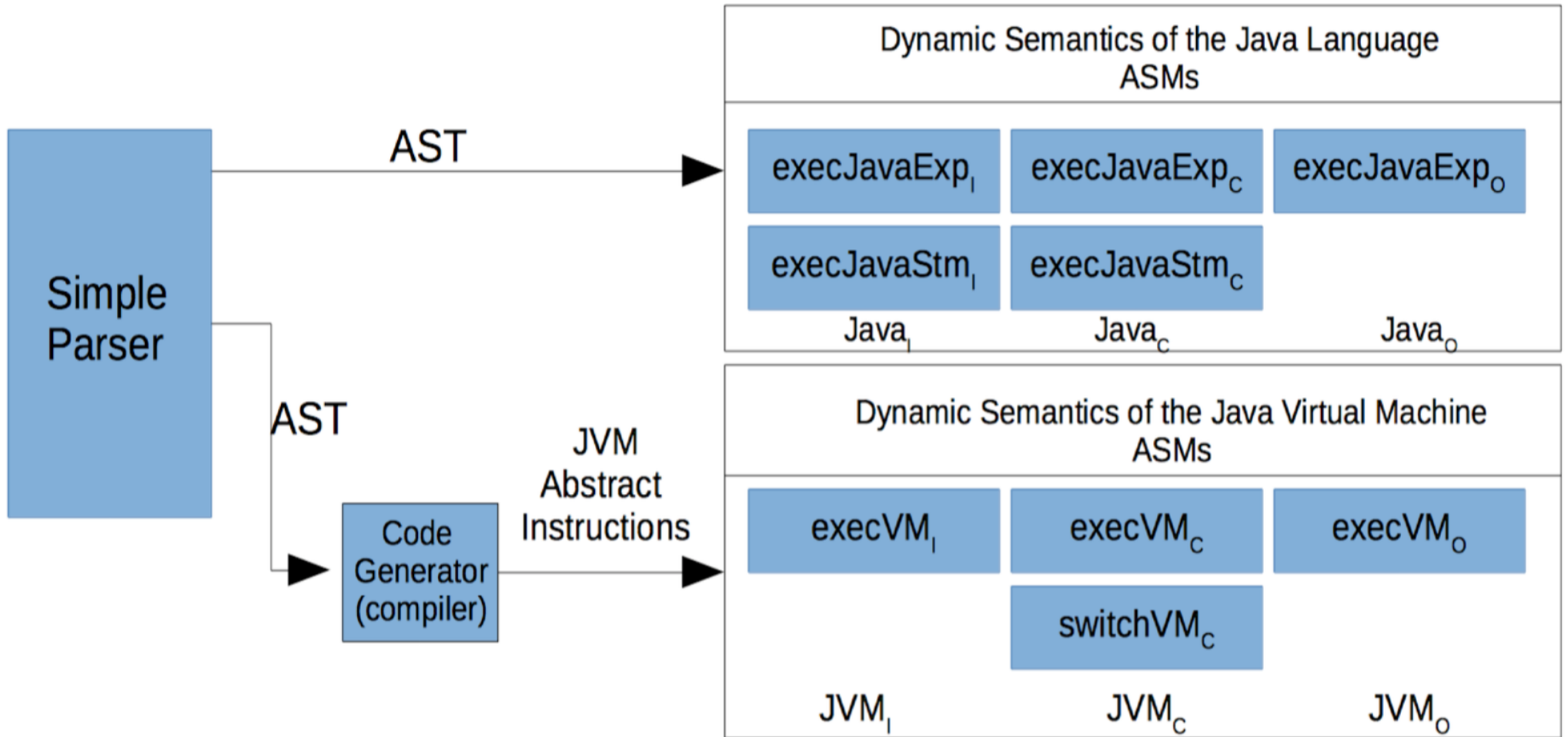
The rules use pattern (left hand side of \rightarrow) matching on concrete syntax. The $:=$ symbol updates a state with the right hand side value

|
 $a == 1 ? b + 2 : b - 2;$

This Java inline condition matches with these set of rules

$$\begin{aligned}
& \text{execJavaExp}_I = \mathbf{case\ context(pos)\ of} \\
& \text{lit} \rightarrow \text{yield}(JLS(\text{lit})) \\
& \\
& \text{loc} \rightarrow \text{yield}(\text{locals}(\text{loc})) \\
& \\
& \text{uop}^\alpha \text{exp} \rightarrow \text{pos} := \alpha \\
& \text{uop}^\blacktriangleright \text{val} \rightarrow \text{yieldUp}(JLS(\text{uop}, \text{val})) \\
& \\
& \alpha \text{exp}_1 \text{bop}^\beta \text{exp}_2 \rightarrow \text{pos} := \alpha \\
& \blacktriangleright \text{val} \text{bop}^\beta \text{exp} \rightarrow \text{pos} := \beta \\
& \alpha \text{val}_1 \text{bop}^\blacktriangleright \text{val}_2 \rightarrow \mathbf{if\ } \neg(\text{bop} \in \text{divMod} \wedge \text{isZero}(\text{val}_2)) \mathbf{\ then} \\
& \quad \text{yieldUp}(JLS(\text{bop}, \text{val}_1, \text{val}_2)) \\
& \\
& \text{loc} = \alpha \text{exp} \rightarrow \text{pos} := \alpha \\
& \text{loc} = \blacktriangleright \text{val} \rightarrow \text{locals} := \text{locals} \oplus \{(\text{loc}, \text{val})\} \\
& \quad \text{yieldUp}(\text{val}) \\
& \\
& \alpha \text{exp}_0 ?^\beta \text{exp}_1 : \gamma \text{exp}_2 \rightarrow \text{pos} := \alpha \\
& \blacktriangleright \text{val} ?^\beta \text{exp}_1 : \gamma \text{exp}_2 \rightarrow \mathbf{if\ val\ then\ pos := \beta\ else\ pos := \gamma} \\
& \alpha \text{True} ?^\blacktriangleright \text{val} : \gamma \text{exp} \rightarrow \text{yieldUp}(\text{val}) \\
& \alpha \text{False} ?^\beta \text{exp} : \blacktriangleright \text{val} \rightarrow \text{yieldUp}(\text{val})
\end{aligned}$$

- This is just mathematical notation of an ASM, it is not executable.
- We want to be able to write Scala code as close as possible to notation (to reduce translation effort) and execute it in computers.



```

execJavaExp1 = case context(pos) of
  lit → yield(JLS(lit))
  loc → yield(locals(loc))

  uop αexp → pos := α
  uop αval → yieldUp(JLS(uop, val))

αexp1 bop βexp2 → pos := α
αval bop βexp2 → pos := β
αval1 bop βval2 → if ¬(bop ∈ divMod ∧ isZero(val2)) then yieldUp(JLS(bop, val1, val2))

  loc = αexp → pos := α
  loc = αval → locals := locals ⊕ {(loc, val)}
                    yieldUp(val)

αexp0 ? βexp1 : γexp2 → pos := α
αval ? βexp1 : γexp2 → if val then pos := β else pos := γ
αtrue ? βval : γexp2 → yieldUp(val)
αfalse ? βexp1 : γval → yeildUp(val)

```

The JBOOK's ASM definition to execute the semantics of the imperative core Java expressions

```

private def execJavaExpI: Unit =
{
  val node = context(pos)
  node match
  {
    case lit:Lit                => yieldResult(JLS(lit))
    case Local(name)            => yieldResult(locals(name))
    case UnaryOp(op, Value(v))  => yieldResultUp(JLS(op, v))
    case UnaryOp(_, exp)        => pos := exp
    case BinaryOp(op, Value(left), Value(right)) => yieldResultUp(JLS(op, left, right))
    case BinaryOp(_, Value(_, exp2)) => pos := exp2
    case BinaryOp(_, exp1, _)    => pos := exp1
    case Asgn(loc, Value(v))    => locals(loc) := v
                                yieldResultUp(v)
    case Asgn(_, exp)           => pos := exp
    case InlineCond(BooleanValue(_, Value(v)), _) => yieldResultUp(v)
    case InlineCond(BooleanValue(_, _, Value(v))) => yieldResultUp(v)
    case InlineCond(BooleanValue(v), exp1, exp2)  => if(v.value) pos := exp1 else pos := exp2
    case InlineCond(exp0, _, _)  => pos := exp0
  }
}

```

Our Scala/
Kiama code

```

private def execVMi(inst:Instruction): Unit =
{
  inst match
  {
    case Prim(p) =>
      val (opdP, ws) = split(opd, argSize(p))
      opd := opdP ::: JVMS(p, ws)
      pc := pc + 1
    case Dupx(s1, s2) =>
      val (opdP, ws1::ws2::_) = splits(opd, List(s1, s2))
      opd := opdP ::: ws2 ::: ws1 ::: ws2
      pc := pc + 1
    case Pop(s) =>
      val (opdP, ws) = split(opd, s)
      opd := opdP
      pc := pc + 1
    case Load(t, x) =>
      if(1 == size(t))
        opd := opd :=+ reg.value(x)
      else
        opd := opd :=+ reg.value(x) :=+ reg.value(x + 1)
      pc := pc + 1
    case Store(t, x) =>
      val (opdP, ws) = split(opd, size(t))
      if(1 == size(t))
        reg(x) := ws(0)
      else
      {
        reg(x) := ws(0)
        reg(x + 1) := ws(1)
      }
      opd := opdP
      pc := pc + 1
    case Goto(offset) => pc := offset
    case Cond(p, offset) =>
      val (opdP, ws) = split(opd, argSize(p))
      opd := opdP
      if(1 == JVMS(p, ws).head)
        pc := offset
      else
        pc := pc + 1
    case Halt() => halt := "Halt"
    case _ =>
  }
}

```

Our Scala/
Kiama code

```

execVMI(instr) = case instr of
  Prim(p) → let (opd', ws) = split(opd, argSize(p))
             if p ∈ divMod → sndArgIsNotZero(ws) then
               opd := opd' · JVMS(p, ws)
               pc := pc + 1
  Dupx(s1, s2) → let (opd', [ws1, ws2]) = splits(opd, [s1, s2])
                 opd := opd' · ws2 · ws1 · ws2
                 pc := pc + 1
  Pop(s) → let (opd', ws) = split(opd, s)
           opd := opd'
           pc := pc + 1
  Load(t, x) → if size(t)=1 then opd := opd · [reg(x)]
              else opd := opd · [reg(x), reg(x + 1)]
              pc := pc + 1
  Store(t, x) → let (opd', ws) = split(opd, size(t))
                if size(t) = 1 then
                  reg := reg ⊕ {(x, ws(0))}
                else
                  reg := reg ⊕ {(x, ws(0)), (x+1, ws(1))}
                opd := opd'
                pc := pc + 1
  Goto(o) → pc := o
  Cond(p, o) → let (opd', ws) = split(opd, argSize(p))
              opd := opd'
              if JVMS(p, ws) then
                pc := o
              else
                pc := pc + 1
  Halt → halt := "Halt"

```

The JBOOK's ASM
definition to execute
the **semantics** of the
imperative core **JVM**
expressions

$\mathcal{E}(\text{lit})$	$= \text{Prim}(\text{lit})$
$\mathcal{E}(\text{loc})$	$= \text{Load}(T(\text{loc}), \overline{\text{loc}})$
$\mathcal{E}(\text{loc} = \text{exp})$	$= \mathcal{E}(\text{exp}) \cdot \text{Dupx}(0, \text{size}(T(\text{exp}))) \cdot \text{Store}(T(\text{exp}), \overline{\text{loc}})$
$\mathcal{E}(!\text{exp})$	$= \mathcal{B}_1(\text{exp}, \text{una}_1) \cdot \text{Prim}(1) \cdot \text{Goto}(\text{una}_2) \cdot \text{una}_1 \cdot \text{Prim}(0) \cdot \text{una}_2$
$\mathcal{E}(\text{uop exp})$	$= \mathcal{E}(\text{exp}) \cdot \text{Prim}(\text{uop})$
$\mathcal{E}(\text{exp}_1 \text{ bop } \text{exp}_2)$	$= \mathcal{E}(\text{exp}_1) \cdot \mathcal{E}(\text{exp}_2) \cdot \text{Prim}(\text{bop})$
$\mathcal{E}(\text{exp}_0 ? \text{exp}_1 : \text{exp}_2)$	$= \mathcal{B}_1(\text{exp}_0, \text{if}_1) \cdot \mathcal{E}(\text{exp}_0) \cdot \text{Goto}(\text{if}_2) \cdot \text{if}_1 \cdot \mathcal{E}(\text{exp}_1) \cdot \text{if}_2$

The JBOOK's definition of the **compiler** for the imperative core Java

```
private def E(node:Node): List[Instruction] =
{
  node match
  {
    case Lit(lit)           => Prim(lit)
    case loc:Local         => Load(T(loc), Bar(loc))
    case Asgn(loc, exp)    => E(expr) ::: Dupx(0, size(T(exp))) ::: Store(T(exp), Bar(loc))
    case UnaryOp(Op.NOT, exp) => val una1 = LabelDef("una1") val una2 = LabelDef("una2")
                                B1(exp, una1) ::: Prim(1) ::: Goto(una2) ::: una1 :::
                                Prim(0) ::: una2
    case uop@UnaryOp(_, exp) => E(exp) ::: Prim(uop)
    case bop@BinaryOp(_, exp1, exp2) => E(exp1) ::: E(exp2) ::: Prim(bop)
    case InlineCond(exp0, exp1, exp2) => val if1 = LabelDef("if1") val if2 = LabelDef("if2")
                                         B1(exp0, if1) ::: E(exp2) ::: Goto(if2) ::: if1 :::
                                         E(exp1) ::: if2
  }
}
```

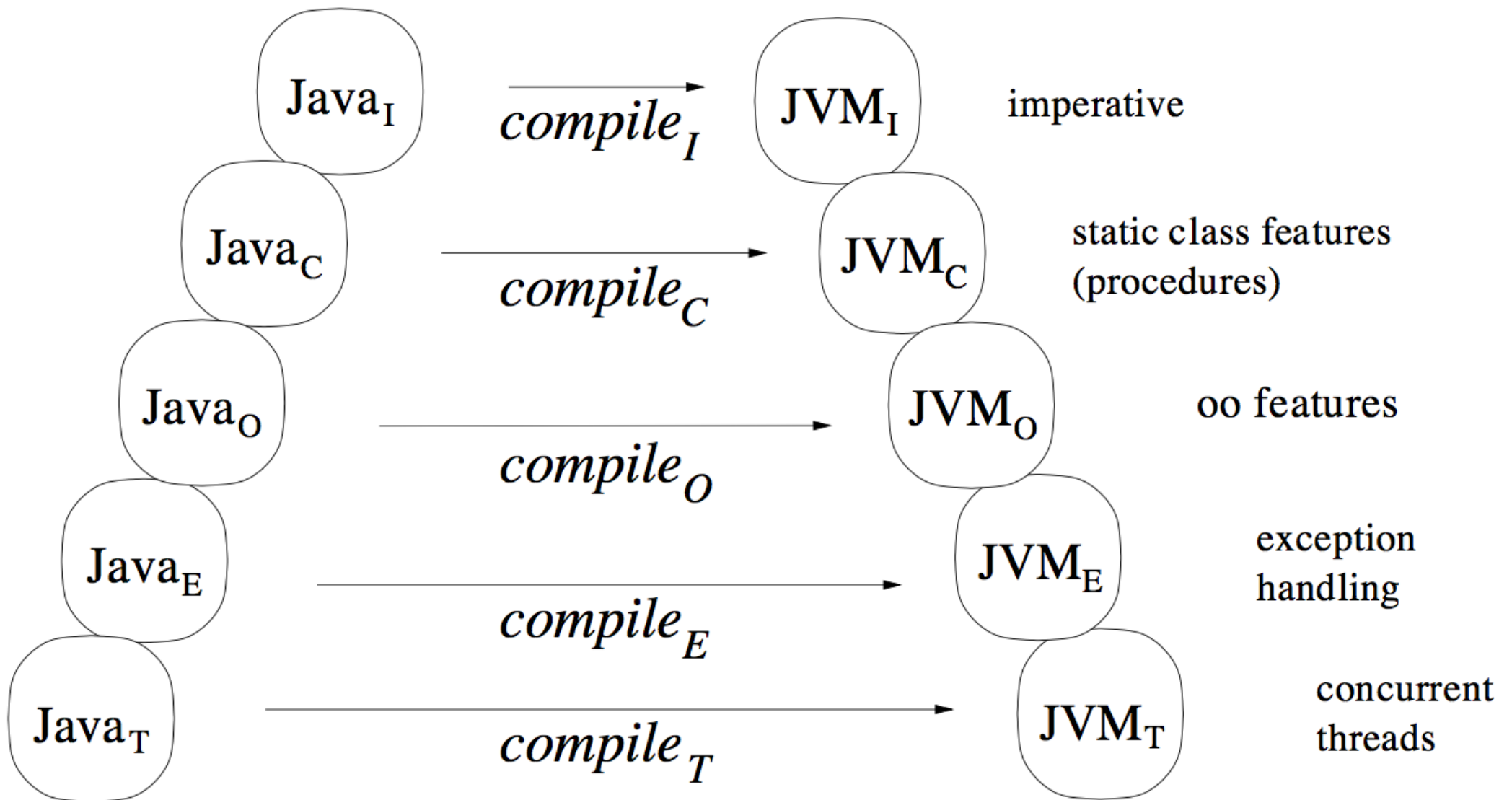
Our Scala/
Kiama code

- Scala has many features which allow us to closely replicate the JBOOK ASM definitions.
- Case classes
- Pattern matching
- Extractor pattern
- Implicit functions
- Functional programming
- Kiama provides basic execution model and definition of states.

Next Step

- Develop a library based on the techniques that we've used in this study.
- —> the techniques can be reused.
- —> ASM definitions may be directly written using Kiama/Scala.
- Try using the concrete syntax in pattern matching, instead of using syntax tree (case classes)

Fin



step	transition rule	Source matched	pos	source at pos
1	${}^{\alpha}exp_0 ? {}^{\beta}exp_1 : {}^{\gamma}exp_2 \rightarrow pos := \alpha$	$1 == 1 ? 1 + 2 : -3$	${}^{\alpha}exp_0$	$1==1$
2	${}^{\alpha}exp_1 \text{ bop } {}^{\beta}exp_2 \rightarrow pos := \alpha$	$1==1$	${}^{\alpha}exp_1$	1
3	$lit \rightarrow yield(JLS(lit))$	1	Val(1)	1
4	$\neg val \text{ bop } {}^{\beta}exp_2 \rightarrow pos := \beta$	$Val(1) == 1 ? 1 + 2 : -3$	${}^{\beta}exp_2$	1
5	$lit \rightarrow yield(JLS(lit))$	1	Val(1)	1
6	$\neg val_1 \text{ bop } \neg val_2 \rightarrow$ $if \neg(bop \in divMod \wedge isZero(val_2))$ $then yieldUp(JLS(bop, val_1, val_2))$	$Val(1) == Val(1)$	Val(true)	$1==1$
7	$\neg val ? {}^{\beta}exp_1 : {}^{\gamma}exp_2 \rightarrow$ $if val then pos := \beta else pos := \gamma$	$val(1) == val(1) ? 1 + 2 : -3$	${}^{\beta}exp_1$	$1+2$
8	${}^{\alpha}exp_1 \text{ bop } {}^{\beta}exp_2 \rightarrow pos := \alpha$	$1 + 2$	${}^{\alpha}exp_1$	1
9	$lit \rightarrow yield(JLS(lit))$	1	Val(1)	1
10	$\neg val \text{ bop } {}^{\beta}exp_2 \rightarrow pos := \beta$	$Val(1) + 2$	${}^{\beta}exp_2$	2
11	$lit \rightarrow yield(JLS(lit))$	2	Val(2)	2
12	$\neg val_1 \text{ bop } \neg val_2 \rightarrow$	$Val(1) + Val(2)$	Val(3)	$1 + 2$
13	${}^{\alpha}true ? \neg val : {}^{\gamma}exp_2 \rightarrow yieldUp(val)$	$Val(true) ? Val(3) : -3$	Val(3)	

Table 2: Step by step evaluation of an inline condition $1 == 1 ? 1+2 : -3$.